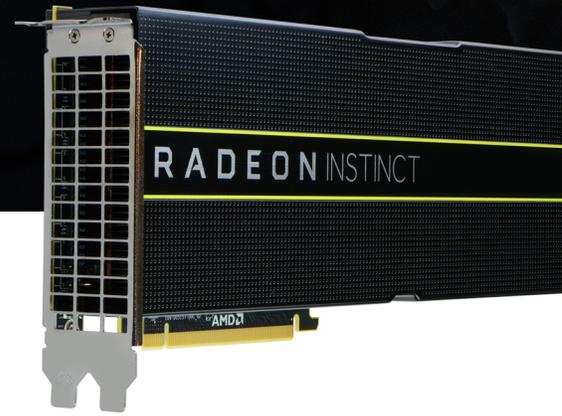


RADEON INSTINCT MI25

The Next Era of Compute and Machine Intelligence



THE WORLD'S FASTEST TRAINING ACCELERATOR FOR MACHINE INTELLIGENCE AND DEEP LEARNING¹

The Radeon Instinct™ MI25 accelerator, designed with the most advanced Next-Gen “Vega” GPU architecture, is the ultimate training accelerator for large scale machine intelligence and deep learning, along with being an optimized open compute workhorse for single-precision HPC-class system workloads.

The MI25 delivers leadership in FP16 and FP32 performance in a passively-cooled single GPU server card with 24.6 TFLOPS of FP16 and 12.3 TFLOPS of FP32 peak performance through its 64 compute units with 4,096 stream processors.¹

The Radeon Instinct MI25's powerful compute engine and advanced memory architecture, combined with AMD's ROCm open software platform and ecosystem, provides a powerful, flexible heterogeneous compute solution that allows datacenter designers to meet the challenges of a new era of compute and Machine Intelligence.

AMD's Radeon Instinct™ MI25, combined with the ROCm open software platform and MIOpen libraries, delivers superior performance per watt for deep learning training deployments in the datacenter.²

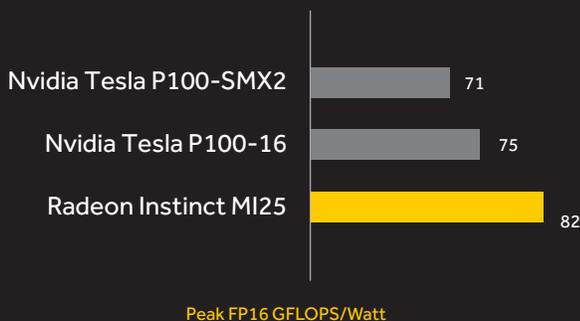
Highlights

- Industry Leading Performance for Deep Learning¹
- Next-Gen “Vega” Architecture
- Advanced Memory Engine
- Large BAR Support for Multi-GPU Peer to Peer
- ROCm Open Software Platform for Rack Scale
- Optimized MIOpen Libraries for Deep Learning
- MxGPU Hardware Virtualization

Key Features

GPU Architecture:	AMD “Vega10”
Stream Processors:	4,096
Performance:	
Half-Precision (FP16)	24.6 TFLOPS
Single-Precision (FP32)	12.3 TFLOPS
Double-Precision (FP64)	768 GFLOPS
GPU Memory:	16GB HBM2
Memory Bandwidth:	Up to 484 GB/s
ECC:	Yes ³
Bus Interface:	PCIe® Gen 3 x16
MxGPU Capability:	Yes
Board Form Factor:	Full-Height, Duel-Slot
Length:	10.5”
Thermal Solution:	Passively Cooled
Standard Max Power:	300W TDP
Warranty:	Three Year Limited ⁴
OS Support:	Linux® 64-bit
ROCm Software Platform:	Yes
Programing Environment:	
ISO C++, OpenCL™, CUDA (via AMD's HIP conversion tool) and Python ⁵ (via Anaconda's NUMBA)	

Superior Performance Per Watt



Note: “Vega 10” is an internal codename only
For more information, visit: [Radeon.com/Instinct](https://www.amd.com/en/instinct)

NEXT-GEN “VEGA” ARCHITECTURE WITH THE WORLD’S MOST ADVANCED MEMORY ARCHITECTURE

The Radeon Instinct MI25 accelerator brings in a new era of compute for the datacenter with its Next-Gen “Vega” architecture delivering superior compute performance via its powerful parallel compute engine and Next-Gen programmable geometry pipeline improving processing efficiencies, while delivering 2x peak throughput-per-clock over previous Radeon architectures.⁶ The Radeon Instinct MI25 provides increased performance density, while decreasing energy consumption per operation making it the perfect solution for today’s demanding workloads in the datacenter.

NEXT-GEN “VEGA” ARCHITECTURE

World’s most advanced GPU compute engine and memory architecture built with cutting-edge 14nm FinFET process, purpose-built to handle big data sets and a diverse range of computational workloads.

HBM2: ULTRA-HIGH MEMORY BANDWIDTH

Combined with AMD’s state of the art Infinity Memory Engine with a newly designed High Bandwidth Cache (HBC) and controller, the MI25 GPU has 16GB of latest HBM2 ECC³ GPU memory with 484 GB/s of memory bandwidth.

PASSIVELY COOLED

The Radeon Instinct MI25 design is a passively-cooled accelerator design for large-scale server deployments.

MxGPU SR-IOV HARDWARE VIRTUALIZATION

Design support for AMD’s MxGPU SR-IOV hardware virtualization technology, the Radeon Instinct MI25 provides a VDI solution with dedicated user GPU resources, data security and version control. Plus, a cost effective licensing model with no additional hardware licensing fees, and a simplified native driver model ensuring operating system and application compatibility.

REMOTE MANAGEABILITY CAPABILITIES

The Radeon Instinct MI25 accelerator has advanced out-of-band manageability circuitry for simplified GPU monitoring in large scale systems. The MI25’s manageability capabilities provide accessibility via I2C, regardless of what state the GPU is in, providing advanced monitoring of a range of static and dynamic GPU information using PMCI compliant data structures including board part detail, serial numbers, GPU temperature, power and other information.

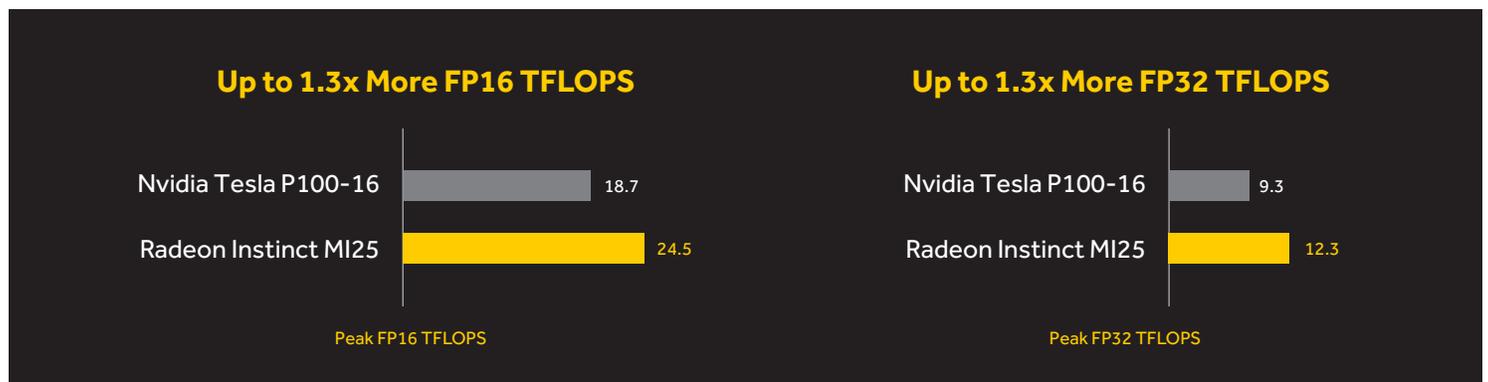
ROCm OPEN SOFTWARE PLATFORM

AMD’s ROCm platform provides a scalable, fully open source software platform optimized for large-scale heterogeneous system deployments with an open source headless Linux driver, HCC compiler, rich runtime based on HSA, tools and libraries.

For more information, visit:
Radeon.com/Instinct
[ROCm.github.io](https://github.com/ROCm)



LEADERSHIP IN FP16 & FP32 PERFORMANCE¹



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FOOTNOTES

1. Measurements conducted by AMD Performance Labs as of June 2, 2017 on the Radeon Instinct™ MI25 "Vega" architecture based accelerator. Results are estimates only and may vary. Performance may vary based on use of latest drivers. PC/system manufacturers may vary configurations yielding different results. The results calculated for Radeon Instinct MI25 resulted in 24.6 TFLOPS peak half precision (FP16) and 12.3 TFLOPS peak single precision (FP32) floating-point performance.

AMD TFLOPS calculations conducted with the following equation: FLOPS calculations are performed by taking the engine clock from the highest DPM state and multiplying it by xx CUs per GPU. Then, multiplying that number by xx stream processors, which exist in each CU. Then, that number is multiplied by 2 FLOPS per clock for FP32. To calculate TFLOPS for FP16, 4 FLOPS per clock were used. The FP64 TFLOPS rate is calculated using 1/16th rate.

External results on the NVidia Tesla P100-16 (16GB card) GPU Accelerator resulted in 18.7 TFLOPS peak half precision (FP16) and 9.3 TFLOPS peak single precision (FP32) floating-point performance.

Results found at: <https://images.nvidia.com/content/tesla/pdf/nvidia-tesla-p100-PCIe-datasheet.pdf>.

External results on the NVidia Tesla P100-SXM2 GPU Accelerator resulted in 21.2 TFLOPS peak half precision (FP16) and 10.6 TFLOPS peak single precision (FP32) floating-point performance.

Results found at: <http://www.nvidia.com/object/tesla-p100.html>

AMD has not independently tested or verified external/third party results/data and bears no responsibility for any errors or omissions therein. RIV-1

2. Measurements conducted by AMD Performance Labs as of June 2, 2017 on the Radeon Instinct™ MI25 "Vega" architecture based accelerator. Results are estimates only and may vary. Performance may vary based on use of latest drivers. PC/system manufacturers may vary configurations yielding different results.

The results calculated for Radeon Instinct MI25 resulted in 82 GFLOPS/watt peak half precision (FP16) or 41 GFLOPS/watt peak single precision (FP32) floating-point performance.

AMD GFLOPS per watt calculations conducted with the following equation: FLOPS calculations are performed by taking the engine clock from the highest DPM state and multiplying it by xx CUs per GPU. Then, multiplying that number by xx stream processors, which exist in each CU. Then, that number is multiplied by 2 FLOPS per clock for FP32. To calculate TFLOPS for FP16, 4 FLOPS per clock were used. The FP64 TFLOPS rate is calculated using 1/16th rate. Once the TFLOPS are calculated, the number is divided by the xxx watts TDP power and multiplied by 1,000 to determine the GFLOPS per watt.

Calculations conducted by AMD Performance Labs as of June 2, 2017 on the NVidia Tesla P100-16 (16GB card) GPU Accelerator to determine GFLOPS/watt by dividing TFLOPS results by 250 watts TDP resulted in 75 GFLOPS per watt peak half precision (FP16) and 37 GFLOPS per watt peak single precision (FP32) floating-point performance.

Sources: <https://images.nvidia.com/content/tesla/pdf/nvidia-tesla-p100-PCIe-datasheet.pdf>

Calculations conducted by AMD Performance Labs as of June 2, 2017 on the NVidia Tesla P100-SXM2 GPU Accelerator to determine GFLOPS/watt by dividing TFLOPS results by 300 watts TDP resulted in 71 GFLOPS per watt peak half precision (FP16) and 35 GFLOPS per watt peak single precision (FP32) floating-point performance.

Sources:

<http://www.nvidia.com/object/tesla-p100.html>

AMD has not independently tested or verified external/third party results/data and bears no responsibility for any errors or omissions therein. RIV-4

3. ECC support is limited to the HBM2 memory and ECC protection is not provided for internal GPU structures.

4. The Radeon Instinct GPU accelerator products come with a three year limited warranty. Please visit www.AMD.com/warranty for details on the specific graphics products purchased. Toll-free phone service available in the U.S. and Canada only, email access is global.

5. Support for Python is planned, but still under development.

6. Data based on AMD Engineering design of Vega. Radeon R9 Fury X has 4 geometry engines and a peak of 4 polygons per clock. Vega is designed to handle up to 11 polygons per clock with 4 geometry engines. This represents an increase of 2.6x. VG-3